SPEC. NO.	TQ3C-8EAF0-E1DDN04-01
DATE	March 28, 2006

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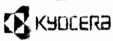
# TYPE: TCG104VG2AA-G00

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- 14. Outline Drawing

Issued

Date: APR. 11.2006



Havato LCD Division

KYOCERA CORPORATION KAGOSHIMA HAYATO PLANT LCD DIVISION

This specification is subject to change without notice. Consult Kyocera before ordering.

Original	Designed by	:Engineering	Confirmed by :QA Dept.		
Issue Date	Prepared	Prepared Checked Approved		Checked	Approved
January 14, 2006	y Yomazaki	H. Tokumord	M.Fujitani	4. Matsumoto	S. Hayoshu

# Caution

- 1. This Kyocera LCD module has been specifically designed for use only in electronic devices in the areas of audio control, office automation, industrial control, home appliances, etc. The modules should not be used in applications where module failure could result in physical harm or loss of life, and Kyocera expressly disclaims any and all liability relating in any way to the use of the module in such applications.
- 2. Customer agrees to indemnify, defend and hold Kyocera harmless from and against any and all actions, claims, losses, damages, liabilities, awards, costs, and expenses, including legal fees, resulting from or arising out of Customer's use, or sale for use, of Kyocera modules in applications.
- 3. Kyocera shall have the right, which Customer hereby acknowledges, to immediately scrap or destroy tooling for Kyocera modules for which no Purchase Orders have been received from the Customer in a two-year period.

Revision Record.

Date March 28,2006		Design	ed by:	Engineering D	ept.	Confirmed by: QA Dept.	
		Prepared		Checked	Approved	Checked	Approved
		J. Yam	azaki	H Tohumori	M. Fritani	4 Matsunoto	S:Hayoshi
Rev. No.	Date		Page		Descripti		·
01	March 2	28, 2006	6	6.Optical Cha Add "Brigh	racteristics tness uniform	ity"	
			7	"6-3.Definiti Change "Th Add "The b	e brightness	is measuring formity"	"
			8	"Re 7-2. CFL		tor" ching connecto	
:			9	8-1. Timing c Change "Ho Change "Ve	haracteristic rizontal sync rtical sync.	. signal"	
			14	14.Outline dr Change out	awing line drawing		

## 1. Application

This data sheet defines the specification for a  $(640 \times R.\,G.\,B) \times 480$  dot, amorphous silicon TFT transmissive color dot matrix type Liquid Crystal Display with CFL backlight. [RoHS Compliant]

#### 2. Construction and Outline

 $(640 \times R.G.B) \times 480$  dots, COG type LCD with CFL backlight.

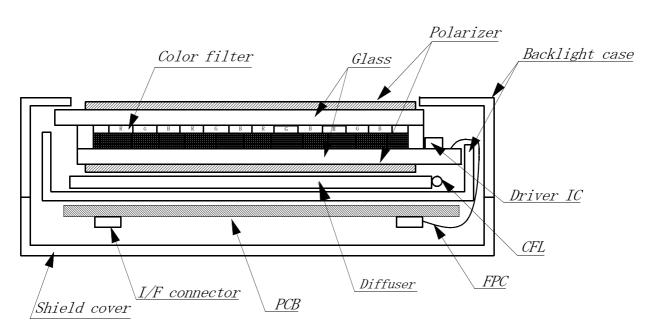
Backlight system : Side-edge type CFL (2 tubes).

Inverter : Option.

Recommended Inverter : CXA-0308 (TDK)

Polarizer : Glare treatment.

Additional circuits: Timing controller, Power supply (3.3V/5.0V input)



This drawing is showing conception only.

# 3. Mechanical Specifications

ITEM	SPECIFICATION	UNIT
Outline dimensions	243 (W) × 185.1 (H) × 10.5 (D)	mm
Effective viewing area	213.2 (W) × 160.4 (H)	mm
Dot number	(640×R.G.B) (W) × 480 (H)	Dots
Dot pitch	0.11 (W) × 0.33 (H)	mm
Display mode *1	Normally Black	_
Mass	(520)	g

<sup>\*1</sup> Due to the characteristics of the LCD material, the color vary with environmental temperature.

## 4. Absolute Maximum Ratings

## 4-1. Electrical absolute maximum ratings

ITEM	SYMBOL	Min.	Max.	UNIT
Power input voltage	VDD	-0.3	6. 0	V
Input signal voltage *1	Vin	-0.3	VDD + 0.3	V

<sup>\*1</sup> Input signals : CK, Hsync, Vsync, RO~R5, GO~G5, BO~B5, DE, SC

#### 4-2. Environmental absolute maximum ratings

ITEM		SYMBOL	Min.	Max.	UNIT
Operating temperature *1		Тор	-10	70	deg. C
Storage temperature *2		Tsto	-20	80	deg. C
Operating humidity	*3	Нор	10	*4	%RH
Storage humidity	*3	Hsto	10	*4	%RH
Vibration		_	*5	*5	_
Shock		_	*6	*6	_

- \*1 Operating temperature means a temperature which operation shall be guaranteed. Since display performance is evaluated at 25 deg.C, another temperature range should be confirmed.
- \*2 Temp. = -20 °C < 48 h , Temp = 80 °C < 168 h Store LCD panel at normal temperature/humidity. Keep it free from vibration and shock. LCD panel that is kept at low or high temperature for a long time can be defective due to the other conditions, even if the temperature satisfies standard. (Please refers to 12. Precautions for use as detail).
- \*3 Non-condensation.
- \*4 Temp.  $\leq$  40 deg.C, 85%RH Max. Temp. > 40 deg.C, Absolute Humidity shall be less than 85% RH at 40 deg.C.

\*5

Frequency	10∼55 Hz	Converted to acceleration value :
Vibration width	0.15 mm	$(0.3 \sim 9 \text{ m/s}^2)$
Interval	10-55-10 Hz	1 minute

2 hours in each direction  $\,$  X/Y/Z (6 hours as total) EIAJ ED-2531

\*6 Acceleration:  $490 \text{m/s}^2$  Pulse width: 11 ms

3 times in each direction :  $\pm X/\pm Y/\pm Z$ .

EIAJ ED-2531

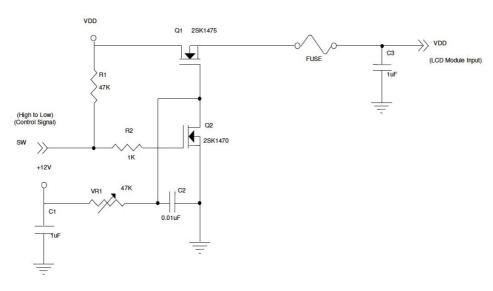
## 5. Electrical Characteristics 5-1. LCD Electrical Characteristics

Temp. =  $25^{\circ}$ C

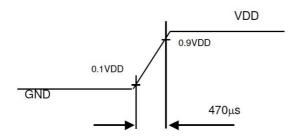
ITEM		SYMBOL	MIN	TYP	MAX	UNIT
Power supply voltage		VDD	3. 0	3.3 / 5.0	5. 25	V
	White (a)	1	_	(495)	1	
Power supply current *2	Black (b)	1		(450)	1	mA
	Vertical Stripe (C)	-	_	(563)	-	
Permissive input ripple v	oltage	Vrp	_	-	(100)	mV
Rush current	IRUSH	_	-	(1)	A	
Input signal voltage (Low	VIL	_		0. 3VDD	V	
Input signal voltage (Hig	VIH	0. 7VDD	_	VDD	V	

st The module is recommended to operate within specification ranges listed above for normal function.

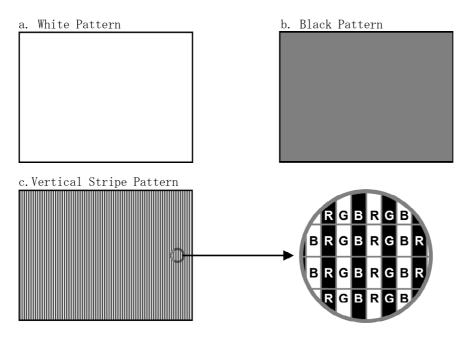
## \*1 Measurement Conditions:



## VDD rising time

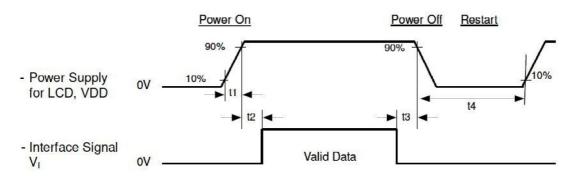


\*2 The specified power supply current is under the conditions at VDD = 3.3V, Ta =  $25^{\circ}\!\text{C}\text{,}$  Fr = 60Hz



## 5-2. Supply Voltage Sequence Condition

To prevent a latch-up or DC operation of LCD module, the power on/off sequence should be as the diagram below.



- \* Timing Characteristics
- $0.5 < t1 \leq 10 ms$ 
  - $0 < t2 \leq 50 ms$
  - 0 < t3 ≦ 50ms
    - t4 ≧ 500ms
- \* Control the supply voltage sequence not to float all signal line when the LCD panel is driving.

## 6. Optical Characteristics

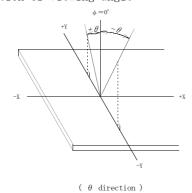
Measuring points =  $\phi$ 6.0mm , Temp. = 25°C

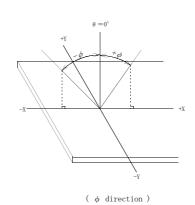
ITEM	Л	SYMBOL CONDITION		MIN	TYP	MAX	UNIT	
Response	Rise	τr	$\theta = \phi$	=0°	_	(14)	_	ms
time	Down	τd	$\theta = \phi$	=0°	_	(9)	_	ms
		0		Upper	_	(85)	_	1
W:		θ	CD > F	Lower	_	(85)	_	deg.
Viewing angle	range	,	CR≧ 5	Left	_	(85)	_	1
		φ		Right	_	(85)	_	deg.
Contrast rati	0	CR	$\theta = \phi = 0^{\circ}$		(600)	(750)	_	_
Brightness		L	IL=5.0mArms.		(360)	(450)	_	$\mathrm{cd/m^2}$
Brightness un	iformity	LU	_		(70)	_	_	%
	Red	Х	$\theta = \phi = 0^{\circ}$		(TBD)	(TBD)	(TBD)	
	кеа	у			(TBD)	(TBD)	(TBD)	
	C.	Х	$\theta = \phi = 0^{\circ}$		(TBD)	(TBD)	(TBD)	
Cl	Green	у	θ-φ	-0	(TBD)	(TBD)	(TBD)	_
Chromaticity coordinates	D1	Х	0 – 1	-0°	(TBD)	(TBD)	(TBD)	
	Blue	у	$\theta = \phi$	-0	(TBD)	(TBD)	(TBD)	
	Wh÷+-	Х	$\theta = \phi$	-0°	(TBD)	(TBD)	(TBD)	
	White	у	θ - φ	-0	(TBD)	(TBD)	(TBD)	

## 6-1. Contrast ratio is defined as follows:

CR = Brightness at all pixels "White"
Brightness at all pixels "Black"

## 6-2. Definition of viewing angle



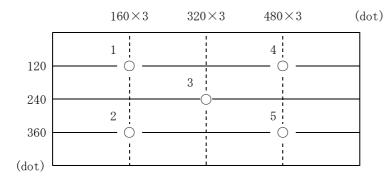


## 6-3. Definition of brightness and brightness uniformity

The brightness is measuring by white brightness at center of display screen(3).

The brightness uniformity is calculated by using following formula.

Brightness uniformity = 
$$\frac{\text{Minimum brightness from 1 to 5}}{\text{Maximum brightness from 1 to 5}} \times 100 \text{ [\%]}$$



- 1) Rating is defined as the average brightness inside the viewing area.
- 2) 30 minutes after CFL is turned on. (Ambient Temp. = $25^{\circ}$ C)
- 3) The inverter should meet the CFL rating conditions;
  -Sine, symmetric waveform without spike in positive and negative.

## 7. Interface signals

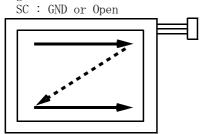
7-1. LCD

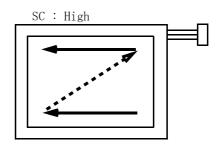
CM1

PIN NO.	SYMBOL	DESCRIPTION	I/0	Note
1	GND	GND	_	
2	СК	Clock signal for sampling each data signal	Ι	
3	Hsync	Horizontal synchronous signal (negative)	Ι	
4	Vsync	Vertical synchronous signal (negative)	Ι	
5	GND	GND	_	
6	R0	RED data signal (LSB)	Ι	
7	R1	RED data signal	Ι	
8	R2	RED data signal	Ι	
9	R3	RED data signal	Ι	
10	R4	RED data signal	Ι	
11	R5	RED data signal (MSB)	Ι	
12	GND	GND	_	
13	GO	GREEN data signal (LSB)	Ι	
14	G1	GREEN data signal	Ι	
15	G2	GREEN data signal	Ι	
16	G3	GREEN data signal	Ι	
17	G4	GREEN data signal	Ι	
18	G5	GREEN data signal (MSB)	I	
19	GND	GND	_	
20	В0	BLUE data signal (LSB)	Ι	
21	B1	BLUE data signal	Ι	
22	B2	BLUE data signal	I	
23	В3	BLUE data signal	I	
24	B4	BLUE data signal	I	
25	B5	BLUE data signal (MSB)	I	
26	GND	GND	_	
27	DE	Signal to settle the horizontal display position(positive)	Ι	
28	VDD	power supply(+3.3V/+5.0V)	_	
29	VDD	power supply(+3.3V/+5.0V)	_	
30	NC	No connect	Ι	*1
31	SC	Scan direction control (GND or Open : normal, High : Reverse)	I	*2

LCD side connector : DF9C-31P-1V(32) (HIROSE)
Recommended matching connector : DF9-31S-1V, DF9A-31S-1V(32) (HIROSE)
DF9B-31S-1V(32), DF9C-31S-1V(32) (HIROSE)

- \* The shield case is connected with GND.
- \*1 Do not input any signal at Pin No.30(Open state). It might happen LCD has a trouble.
- \*2 Scanning Direction





7-2. CFL CN2

CIVZ		
PIN NO.	SYMBOL	DESCRIPTION
1	COLD	Inverter output low voltage side
2	HOT	Inverter output High voltage side
3	HOT	Inverter output High voltage side

LCD side connector : BHR-03VS-1 (JST)
Recommended matching connector : SM03(4.0)B-BHS-1TB (JST)

: SM03(4.0)B-BHS-1-TB(LF)(SN) (JST) • • • (RoHS)

\* Please be careful NOT to connect inversely an inverter-output high voltage side to the CFL low voltage side. It may result in damage or electric shock.

## 8. Timing Characteristics of input signals

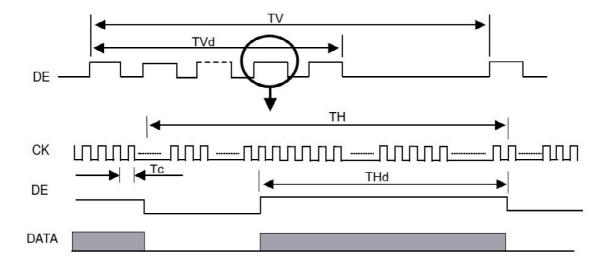
## 8-1. Timing characteristics

ITEM		SYMBOL	MIN	TYP	MAX	UNIT	NOTE
Clock	Frequency	1/Tc	(21)	(25. 175)	(29)	MHz	
Clock	Duty ratio		(0.4)	(0.5)	(0.6)	_	
Data	Set up time	Tds	(8)	_	_	ns	
Data	Hold time	Tdh	(12)	_	_	ns	
Horizontal sync.	Cycle	TH	(690)	(800)	(1, 279)	clock	*1
signal	Display	THd	(640)	(640)	(640)	clock	
	Blank	THb	(80)	(160)	(TH-THd)	clock	
Vertical sync.	Frame frequency	Fr	(56)	(60)	(75)	Hz	
signal	Cycle	TV	(482)	(525)	(959)	line	*2
	Display	TVd	(480)	(480)	(480)	line	
	Blank	TVb	(2)	(45)	(TV-TVd)	line	

<sup>\*</sup> In case of lower frequency, the deterioration of the display quality, flicker etc., may occur.

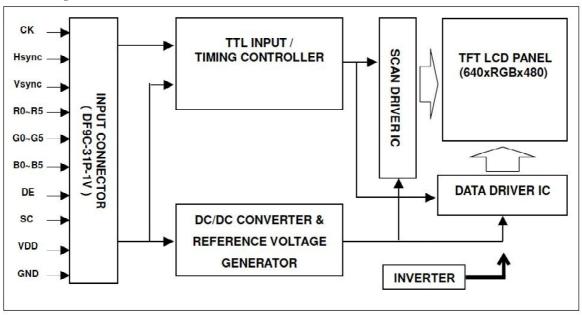
$$*2 TV = TVd + TVb$$

<sup>\*3</sup> Input signal timing diagram



<sup>\*1</sup> TH = THd + THb

#### 8-2. Block diagram



### 9. Backlight Characteristics

Temp. =  $25^{\circ}$ C

ITEM	SYMBOL	MIN.	TYP.	MAX.	NOTE	
Starting		VS	_		(1,325) Vrms.	−10 °C
discharge Voltage	*1	VS	_		(830) Vrms.	25 ℃
Discharging tube current	*2	IL	(2.0) mArms.	(5.0) mArms.	(6.0) mArms.	_
Discharging tube voltage		VL	_	(490) Vrms.	_	IL=5.0mArms.
Operating life	*3	Т	(36,000) h	(50,000) h		IL=5.0mArms.
Operating frequency	*4	F	(40) kHz		(100) kHz	_

- \*1 The Non-load output voltage (VS) of the inverter should be designed to have some margin, because VS may increase due to the leak current which may be caused by wiring of CFL cables. (Reference value: (1,720) Vrms Min.)
- \*2 We recommend that you should set the discharging tube current at lower than typical value so as to prevent the heat accumulation of CFL tube from deteriorating a performance of the LCD.
- \*3 End of life is defined as when the illuminance or quantity of light has decreased to 50% of the initial value. Illuminance of light will drastically decrease when LCD is operated at lower temperature for long hours.
- \*4 The driving frequency of the CFL may interfere with the horizontal synchronous signal ,leaving interference stripes on the display. So please evaluate LCD panels beforehand.

  To avoid interference stripes, we recommend to separate as far as possible the CFL frequency from the horizontal synchronous signal and its high harmonic frequency.
- \* There may be cases where interface noise on LCD PCB, generated by high-voltage products such as inverters, may leave stripes on the display. Please be careful when designing a mold to take into consideration that the inverter shall be located as far as possible from PCB. Shield protection may be effective.

## 10. Lot Number Identification

The lot number shall be indicated on the back of the backlight case of each LCD.

$\begin{array}{c ccccccccccccccccccccccccccccccccccc$									
①YEAR ②MONTH ③DATE ④Version Number ⑤Country of origin(Japan or China)									
YEAR	YEAR         2006         2007         2008         2009         2010         2011								
CODE	6	7	8	9	0	1			
	Ī	Ī		Ī	Ī				
MONTH	JAN.	FEB.	MAR.	APR.	MAY.	JUN.			
CODE	CODE         1         2         3         4         5         6								
MONTH	JUL.	AUG.	SEP.	OCT.	NOV.	DEC.			
CODE	7	8	9	X	Y	Z			

## 11. Warranty

#### 11-1. Incoming inspection

Please inspect the LCD within one month after your receipt.

### 11-2. Production Warranty

Kyocera warrants its LCDs for a period of 12 months after receipt by the purchaser, and within the limits specified. Kyocera shall, by mutual agreement, replace or rework defective LCDs that are shown to be Kyocera's responsibility.

#### 12. Precautions for use

#### 12-1. Installation of the LCD

- 1. A transparent protection sheet shall be added to protect the LCD and its polarizers.
- 2. The LCD shall be installed so that there is no pressure on the LSI chips.
- 3. The LCD shall be installed flat, without twisting or bending.
- 4. The display window size should be the same as the effective viewing area.
- 5. In case you use outside frame of effective viewing area as outward appearance of your product, unevenness of its outward appearance is out of guarantee.
- 6. Do not pull the CFL lead wires and do not bend the root of the wires. Housing should be designed to protect CFL lead wires from external stress.

#### 12-2. Static Electricity

1. Since CMOS ICs are mounted directly onto the LCD glass, protection from static electricity is required. Operator should wear ground straps.

#### 12-3. LCD Operation

- 1. The LCD shall be operated within the limits specified. Operation at values outside of these limits may shorten life, and/or harm display images.
- 2. Operation of the LCD at temperature below the limit specified may cause image degradation and/or bubbles.

It may also change the characteristics of the liquid crystal.

This phenomenon may not recover. The LCD shall be operated within the temperature limits specified.

#### 12-4. Storage

- 1. The LCD shall be stored within normal temperature and humidity. Store in a dark area, and protected the LCD from direct sunlight or fluorescent light.
- 2. Always store the LCD so that it is free from external pressure onto it.

#### 12-5. Screen Surface

- 1.  $\underline{\text{DO NOT}}$  store in a high humidity environment for extended periods.  $\overline{\text{Image}}$  degradation, bubbles, and/or peeling off of polarizer may result.
- 2. The front polarizer is easily scratched or damaged.
  - Prevent touching it with any hard material, and from being pushed or rubbed.
- 3. The LCD screen may be cleaned with a soft cloth or cotton pad.
  - Methanol, or Isopropyl Alcohol may be used, but insure that all solvent residue is removed.
- 4. Water may cause damage or discoloration of the polarizer.
  - Clean any condensation or moisture from any source immediately.
- 5. Always keep the LCD free from condensation during testing. Condensation may permanently spot or stain the polarizers.
- 6. Do not disassemble LCD module because it will result in damage.
- 7. Please do not use solid-base image pattern for long hours because a temporary afterimage may appear. We recommend to use screen saver etc. in cases where a solid-base image pattern must be used.
- 8. A transparent protection sheet is attached to the polarizer. Please remove the protection film slowly before use, paying attention to static electricity.
- 9. This Kyocera LCD module has been specifically designed for use in general electronic devices, but not for use in a special environment such as usage in an active gas.

  Hence, when the LCD is supposed to be used in a special environment, evaluate the LCD thoroughly beforehand and do not expose the LCD to chemicals such as an active gas.

## 13. Reliability Data / Environmental Test

TEST ITEM	TEST CONDITION	TEST TIME	RESULT			
High Temp. Atmosphere	80°C	240 h	Display Quality Display Function Current Consumption	: T. B. D. : T. B. D. : T. B. D.		
Low Temp. Atmosphere	−30°C	240 h	Low Temp. Bubble Solid Crystallization of Liquid Crystal Display Quality Display Function Current Consumption	: T. B. D. : T. B. D. : T. B. D. : T. B. D. : T. B. D.		
High Temp. Humidity Operation	60℃ 90 %RH	240 h	Display Quality Display Function Peel-off of Organic Sealing Current Consumption	: T. B. D. : T. B. D. : T. B. D. : T. B. D.		
Temp. Cycle	-30°C 0.5 h R.T. 0.5 h 80°C 0.5 h	10 cycles	Display Quality Display Function Peel-off of Organic Sealing Bubble on Cell	: T. B. D. : T. B. D. : T. B. D. : T. B. D.		
High Temp. Operation	70°C	500 h	Display Quality Current Consumption	: T. B. D. : T. B. D.		

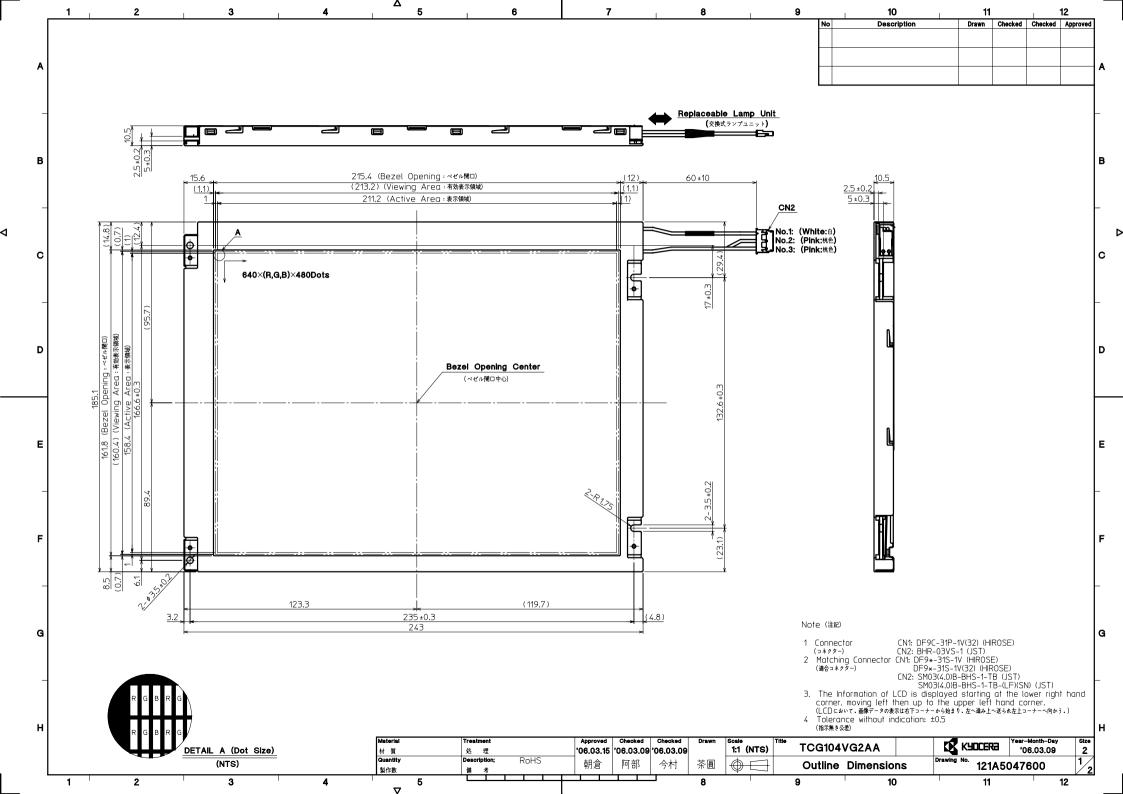
<sup>\*</sup> Each test item uses a test LCD only once. The tested LCD is not used in any other tests.

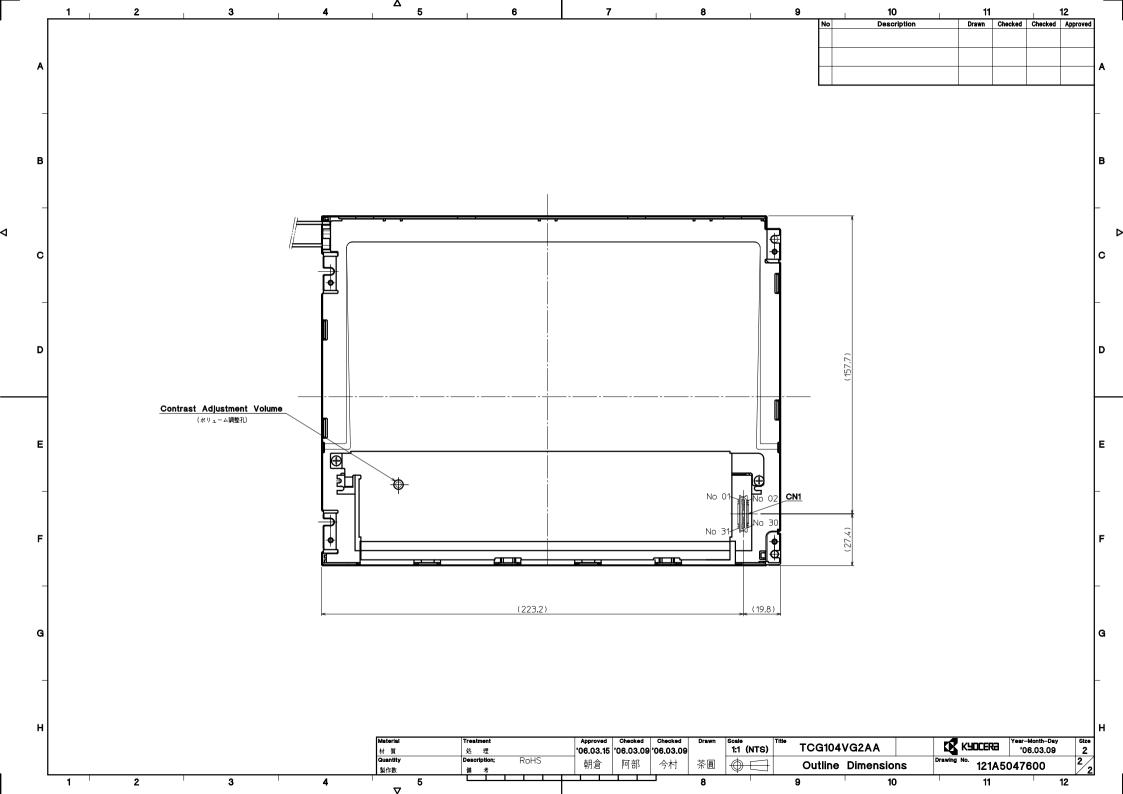
st The LCD is tested in circumstances in which there is no condensation.

<sup>\*</sup> The tested LCD is inspected after 24 hours of storage at room temperature and room humidity after each test is finished.

st The reliability test is not an out-going inspection.

<sup>\*</sup> The results of the reliability test are for your reference purpose only. The reliability test is conducted only to examine the LCD's capability.





SPEC. NO.	TQ3C-8EAF0-E2DDN04-00
DATE	January 14, 2006

FOR	,	

# KYOCERA INSPECTION STANDARD

<u>TYPE</u>: TCG104VG2AA-G00

KYOCERA CORPORATION KAGOSHIMA HAYATO PLANT LCD DIVISION

Original	Designed	by :Engineer	ing Dept.	Confirmed by :QA Dept.		
Issue Date	Prepared	Checked	Approved	Checked	Approved	
January 14, 2006	y Yamajaki	H. Tokumon	M. Fujitani	4. Matsumoto	S. Hyona!	

## Revision Record

Date		Design	ed by:	: Engineering Dept. Confirmed by: QA		QA Dept.	
рате	<del>-</del>	Prepa	red	Checked	Approved	Checked	Approved
Rev. No.	Date		Page		Descriptio	ons	

# 1) Note

	Note						
General	shall be revi determined by 2. Inspection Co Luminance	Inspection distance $:300$ mm (from the sample) Temperature $:25~\pm~5~{}^{\circ}{\rm C}$					
Definition of Inspection item	Dot defect	Bright dot defect	The dot is constantly "on" when power applied to the LCD, even when all "Black" data sent to the screen.  Inspection tool:5% Transparency neutral density filter.  Count dot:If the dot is visible through the filter  Don't count dot:If the dot is not visible through the filter.  RGBRGBRGB RGBRGB RGBRGB RGBRGBRGB				
		Black dot defect	The dot is constantly "off" when power applied to the LCD, even when all "white" data sent to the screen.				
	External inspection	Adjacent dot	Adjacent dot defect is defined as two or more bright dot defects or black dot defects.				
			R G B R G B R G B R G B R G B R G B R G B				
		Bubble, Scratches, Foreign particle (Polarizer, Cell, Backlight)	Visible operating (all pixcels "Black" or "White") and non operating.				
		Appearance inspection	Does not satisfy the value at the spec.				
	Others	CFL wires	Damaged to the CFL wires, connector, pin, functional failure or appearance failure.				
	Definition of size	Definition of ci	Definition of linear size				

# 2) Standard

Classification		Inspection item		Judgement standard				
defect (in LCD	Dot defect	Bright dot de	fect	Acceptable number			t dots defects	
glass)		Black dot def	ect	Acceptable number : 5 black dots defects Black dot spacing : 5 mm or more				
		2 dots join Bright dot defect		Acceptable number	er : 2			
			Black dot defect	Acceptable number	er : 3			
		3 or more dot	s join	Acceptable number	er : 0			
		Total dot def	ects	Acceptable number	er : 5	Max		
	Others	White dot, Da	rk dot	Size(mm	1)	A	cceptable Number	
		(Circle)		d<	0. 2		(neglected)	
				0. 2 < d≦0	0. 4		5	
				0.4 <d≦< td=""><td>0. 5</td><td></td><td>3</td></d≦<>	0. 5		3	
				0.5 <d< td=""><td></td><td></td><td>0</td></d<>			0	
	pection	Polarizer(Scr	Polarizer(Scratches)		Length(n	nm)	Acceptable Number	
(Defect o Polarize	er or			W≦0.1	_		(neglected)	
between -er and					L≦5.0		(neglected)	
glass)				0.1<₩≦0.3	5.0 <l< td=""><td></td><td colspan="2">0</td></l<>		0	
				0.3 <w< td=""><td>-</td><td></td><td colspan="2">0</td></w<>	-		0	
		Polarizer Tou	ch nanel					
		(Bubble, Dent	) paner	Size(mm)		A	cceptable Number	
				d<0.2		(neglected)		
				$0.2 < d \le 0.3$			5	
				0.3 <d≤0.5< td=""><td></td><td>3</td></d≤0.5<>			3	
				0.5 <d< td=""><td></td><td colspan="2">0</td></d<>		0		
		Foreign Parti shape)	cle(Circular	Size(mm)		Acceptable Number		
		Shape/		d<0				
				0. 2 < d≦0		(Hegrected)		
				0. 4 < d ≤ 0		3		
				0.5 <d< td=""><td></td><td colspan="2">0</td></d<>		0		
		Foreign Parti (Linear shape	cle ),	Width (mm)	Length(m	nm)	Acceptable Number	
		Scratches		W≦0.03	_		(neglected)	
					L≦	2.0	(neglected)	
				$0.03 < W \le 0.1$	2. 0 < L ≤ 4. 0		3	
					4.0 <l< td=""><td></td><td>0</td></l<>		0	
				0.1 <w< td=""><td>-</td><td></td><td>(According to Circular shape)</td></w<>	-		(According to Circular shape)	